

SILICON DARLINGTON POWER TRANSISTORS

NPN epitaxial base transistors in monolithic Darlington circuit for audio output stages and general amplifier and switching applications. PNP complements are BDV64, 64B and 64C.

QUICK REFERENCE DATA

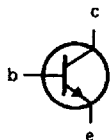
		BDV65	A	B	C
Collector-base voltage (open emitter)	V_{CB0}	max. 60	80	100	120 V
Collector-emitter voltage (open base)	V_{CEO}	max. 60	80	100	120 V
Collector current (DC)	I_C	max.	12		A
Total power dissipation up to $T_{mb} = 25\text{ }^\circ\text{C}$	P_{tot}	max.	125		W
Junction temperature	T_j	max.	150		$^\circ\text{C}$
D.C. current gain					
$I_C = 1\text{ A}; V_{CE} = 4\text{ V}$	h_{FE}	typ.	1500		
$I_C = 5\text{ A}; V_{CE} = 4\text{ V}$	h_{FE}	>	1000		
Cut-off frequency					
$I_C = 5\text{ A}; V_{CE} = 4\text{ V}$	f_{hfe}	typ.	70		kHz

MECHANICAL DATA

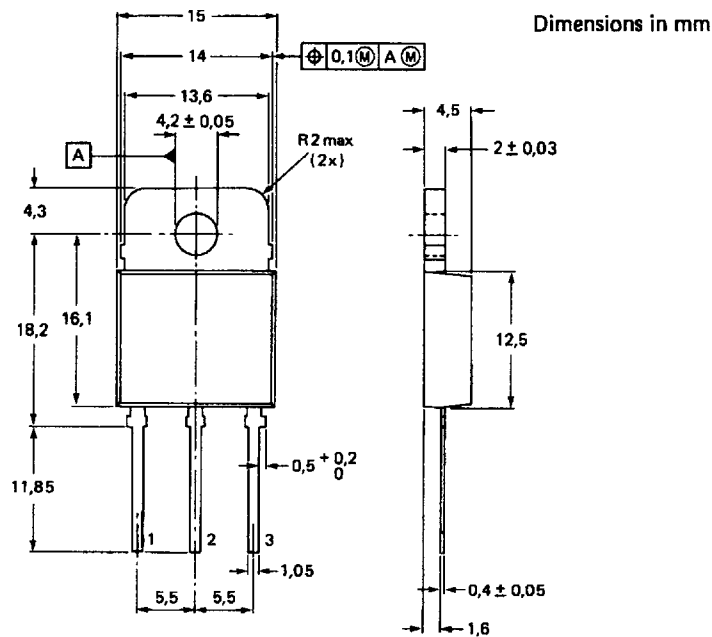
Fig. 1 SOT-93.

Collector connected to mounting-base.

Pinning:
1 = base
2 = collector
3 = emitter



See also chapters Mounting instructions and Accessories.



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CIRCUIT DIAGRAM

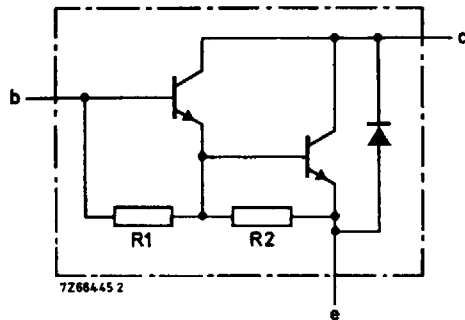


Fig. 2.
R1 typical 5 kΩ
R2 typical 80 Ω.

RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

		BDV65	A	B	C
Collector-base voltage (open emitter)	V_{CB0}	max. 60	80	100	120 V
Collector-emitter voltage (open base)	V_{CE0}	max. 60	80	100	120 V
Emitter-base voltage (open collector)	V_{EB0}	max. 5	5	5	5 V
Collector current (d.c.)	I_C	max.	12		A
Collector current (peak value)	I_{CM}	max.	20		A
Base current (d.c.)	I_B	max.	0,5		A
Total power dissipation up to $T_{mb} = 25\text{ }^\circ\text{C}$	P_{tot}	max.	125		W
Storage temperature	T_{stg}		-65 to + 150		$^\circ\text{C}$
Junction temperature	T_j	max.	150		$^\circ\text{C}^*$

THERMAL RESISTANCE

From junction to mounting base	$R_{th\ j-mb}$		1		K/W*
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CHARACTERISTICS

$T_j = 25\text{ }^\circ\text{C}$ unless otherwise specified.

Collector cut-off currents

$I_E = 0; V_{CB} = V_{CB0max}$	I_{CB0}	<	400		μA
$I_E = 0; V_{CB} = \frac{1}{2}V_{CB0max}; T_j = 150\text{ }^\circ\text{C}$	I_{CB0}	<	2		mA
$I_B = 0; V_{CE} = \frac{1}{2}V_{CE0max}$	I_{CE0}	<	0,2		mA

Emitter cut-off current

$I_C = 0; V_{EB} = 5\text{ V}$	I_{EB0}	<	5		mA
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* Based on maximum average junction temperature in line with common industrial practice. The resulting higher junction temperature of the output transistor part is taken into account.

CHARACTERISTICS

 $T_j = 25\text{ }^\circ\text{C}$ unless otherwise specified.

D.C. current gain*

 $I_C = 1\text{ A}; V_{CE} = 4\text{ V}$ $I_C = 5\text{ A}; V_{CE} = 4\text{ V}$ $I_C = 10\text{ A}; V_{CE} = 4\text{ V}$

h_{FE}	typ.	1500
h_{FE}	>	1000
h_{FE}	typ.	1750

Base-emitter voltage*

 $I_C = 5\text{ A}; V_{CE} = 4\text{ V}$

V_{BE}	<	2,5 V**
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Collector-emitter saturation voltage*

 $I_C = 5\text{ A}; I_B = 20\text{ mA}$

V_{CEsat}	<	2 V
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Collector capacitance at $f = 1\text{ MHz}$ $I_E = I_e = 0; V_{CB} = 10\text{ V}$

C_c	typ.	150 pF
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Cut-off frequency

 $I_C = 5\text{ A}; V_{CE} = 4\text{ V}$

f_{hfe}	typ.	70 kHz
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Diode, forward voltage

 $I_F = 5\text{ A}$ $I_F = 12\text{ A}$

V_F	typ.	1,2 V
V_F	typ.	2 V

Switching times (see also Fig. 4)

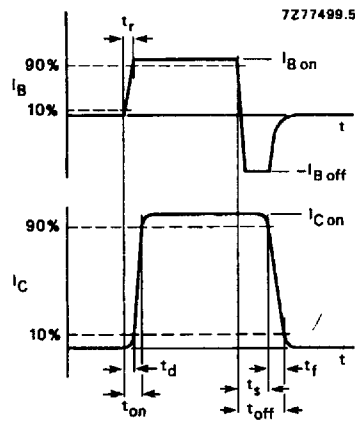
 $I_{Con} = 5\text{ A}; I_{Bon} = -I_{Boff} = 20\text{ mA}; V_{CC} = 16\text{ V}$

Turn-on time

Fall time

Turn-off time

t_{on}	typ.	1 μs
t_f	typ.	3 μs
t_{off}	typ.	6 μs

Fig. 3 Waveforms showing $t_{on}; t_s + t_f = t_{off}$.* Measured under pulse conditions: $t_p < 300\text{ }\mu\text{s}; \delta < 2\%$.** V_{BE} decreases by about 3,6 mV/K with increasing temperature.

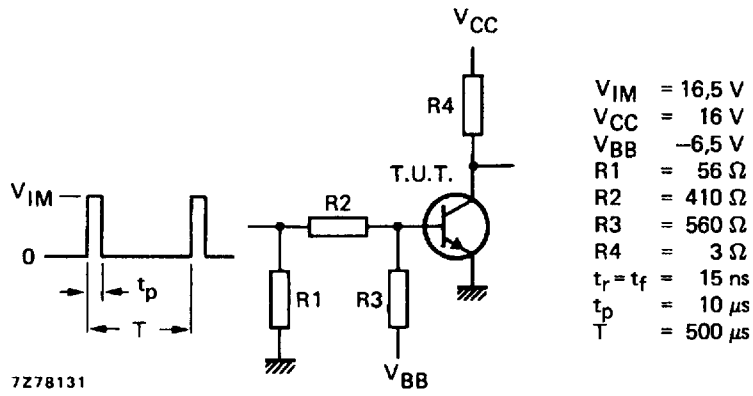


Fig. 4 Switching times test circuit.

Turn-off breakdown energy with inductive load (see also Fig. 5).

$I_{Con} = 6,3$ A; $-I_{Boff} = 0$; $t_p = 1$ ms; $T = 100$ ms

$E_{(BR)} > 100$ mJ

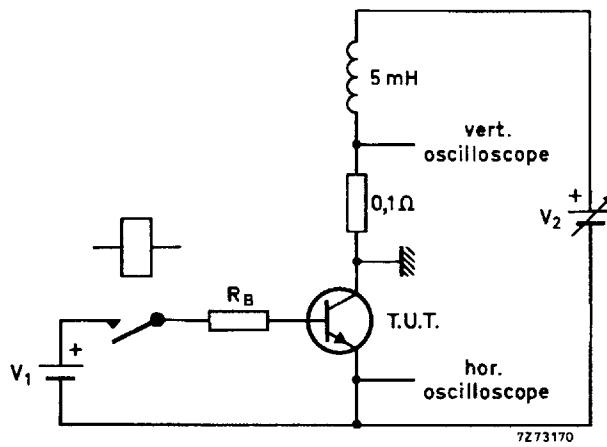


Fig. 5 Test circuit; $V_1 = 12$ V; $R_B = 270$ Ω .

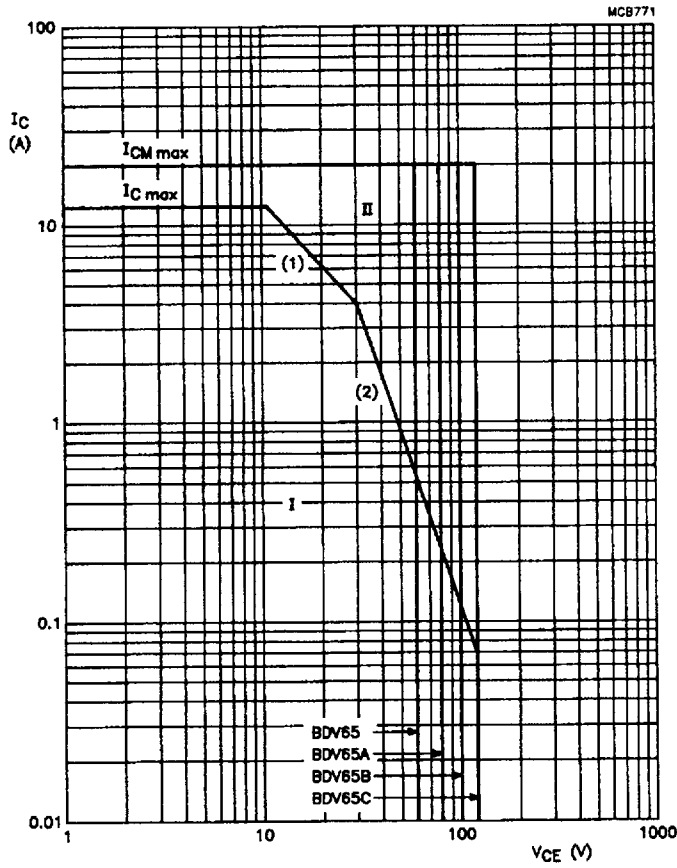


Fig. 6 Safe Operating Area; $T_{mb} \leq 25^\circ\text{C}$.

- I Region of permissible d.c. operation.
- II Permissible extension for repetitive pulse operation.
- (1) $P_{tot\ max}$ and $P_{peak\ max}$ lines.
- (2) Second breakdown limits.

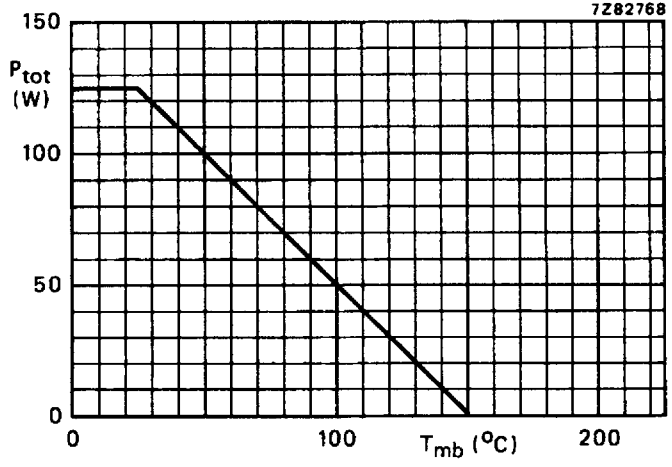


Fig. 7 Power derating curve.

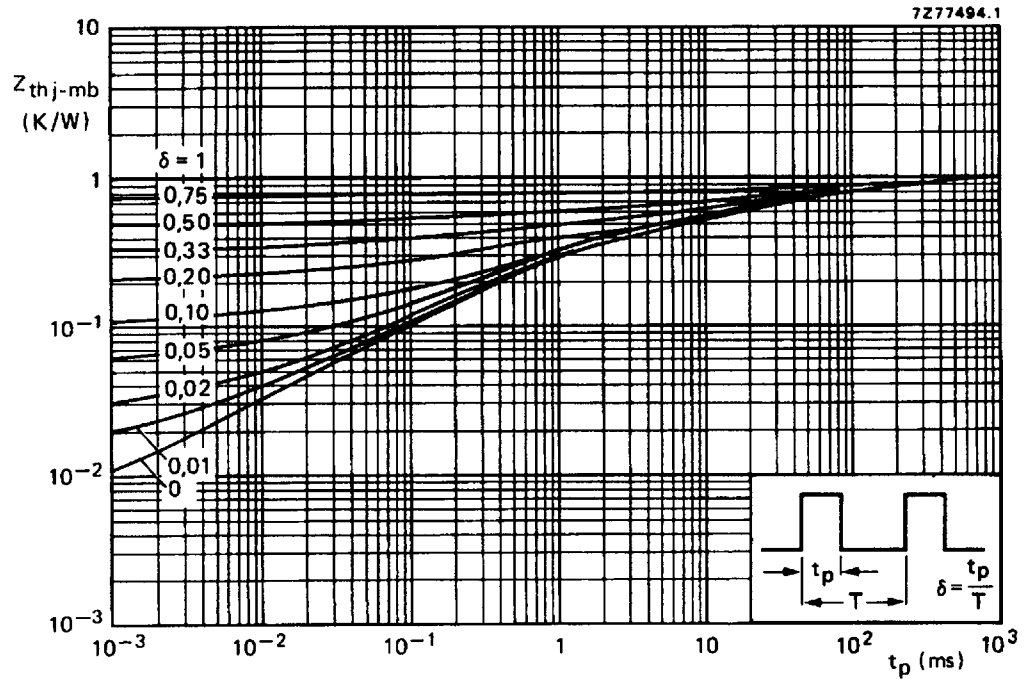


Fig. 8 Pulse power rating chart.

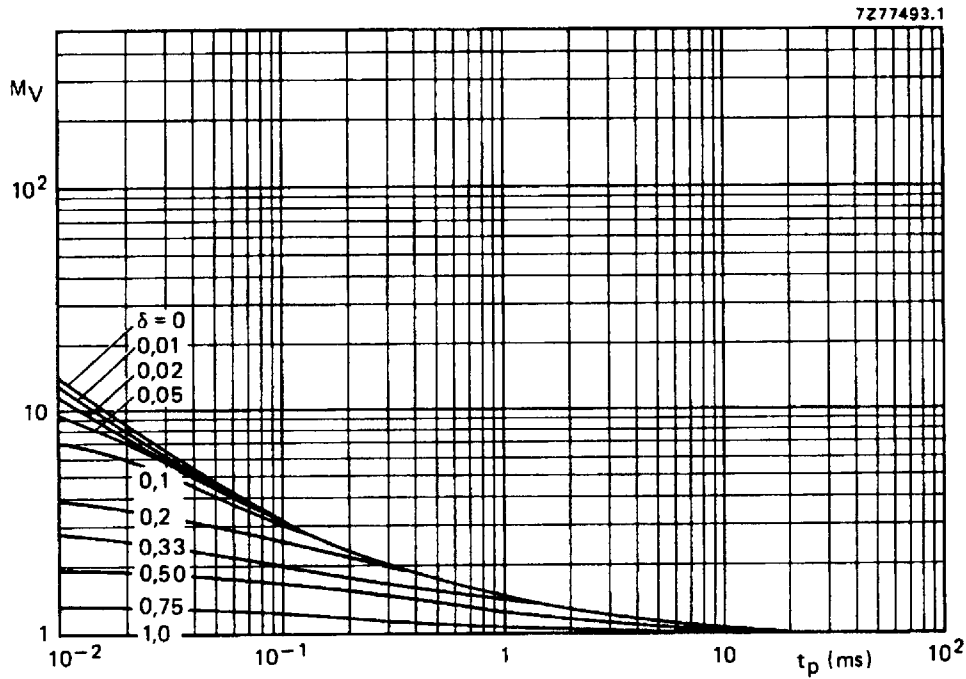


Fig. 9 S.B. voltage multiplying factor at the I_{Cmax} level.

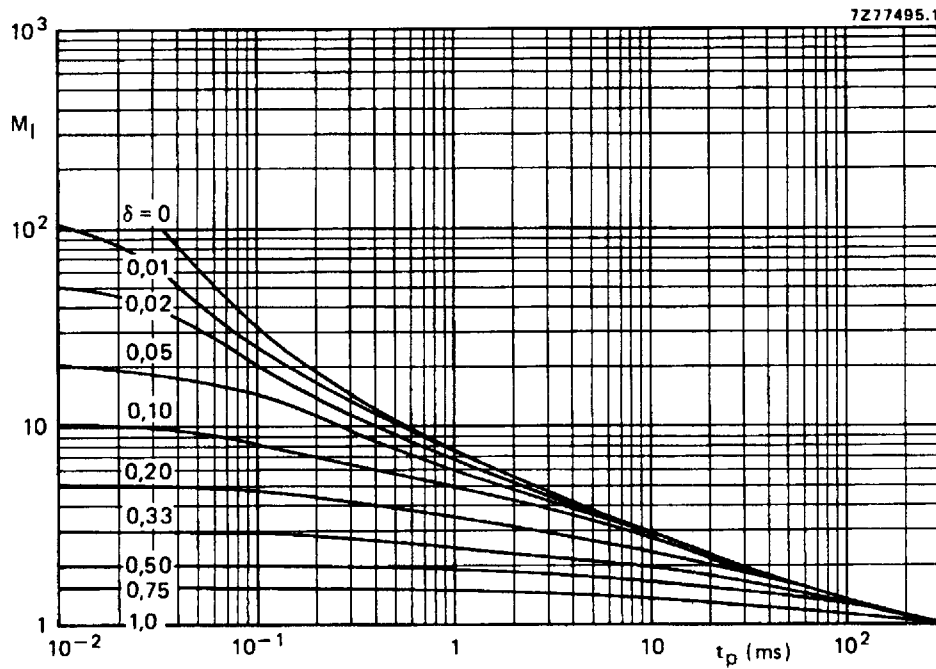


Fig. 10 S.B. current multiplying factor at the V_{CE0max} level (100 V).

