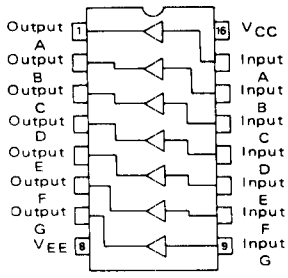
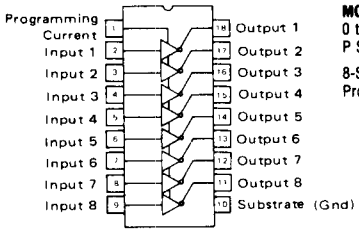


MC3490
0 to 70°C
P Suffix — Case 648

7-Digit Anode Driver with High Logic Level Input Required for Activation

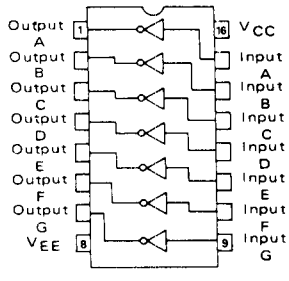
Breakdown Voltage Volts Min.	Input Voltage (OFF-State) Volts Min.	Input Voltage (ON-State) Volts Max.	Input Current μA Max.
48	−5.0	−2.0	450



MC3491
0 to 70°C
P Suffix — Case 701

8-Segment Cathode Driver with Programmable Current

Breakdown Voltage Volts Min.	Current Deviation (All 8 Outputs) % Max.	Output Current Compliance Voltage Volts Range
80	10	5.0 to 50

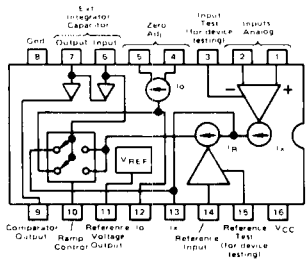


MC3494
0 to 70°C
P Suffix — Case 648

7-Digit Anode Driver with Low Logic Level Input Required for Activation

Breakdown Voltage Volts Min.	Input Voltage (OFF-State) Volts Max.	Input Voltage (ON-State) Volts Min.	Input Current μA Max.
48	−2.0	−5.0	−350

AD/DA CONVERSION

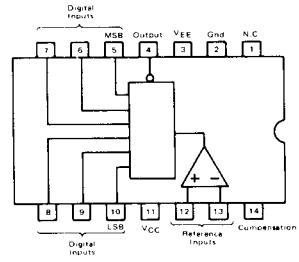


MC1505
−55 to 135°C
L Suffix — Case 620

MC1405
0 to 70°C
L Suffix — Case 620

A dual ramp subsystem which can provide accuracies to 4½ BCD digits or 13 binary bits. May be used with McMOS or MTTL logic systems. Mates with MC14435 for complete 3½ BCD Converter function.

Linearity Error % Max.	Voltage Reference Volts	Temperature Coefficient of Reference %/°C	I_{CC} @ V_{CC} = 5.0 V mA Max.
±0.05	1.15 to 1.35	0.005	12

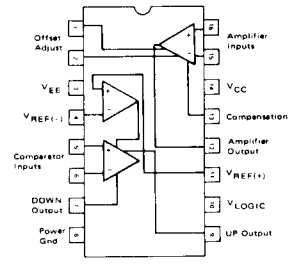


MC1506
−55 to 125°C
L Suffix — Case 632

MC1406
0 to 70°C
L Suffix — Case 632

6-Bit Multiplying Digital-to-Analog Converters

Accuracy % Min.	t_{PHL}, t_{PLH} ns Max.	Output Current @ V_{EE} = −5.0 V mA Range	P_D @ V_{EE} = −5.0 V mW Max.
±0.78	50	0 to 2.1	120

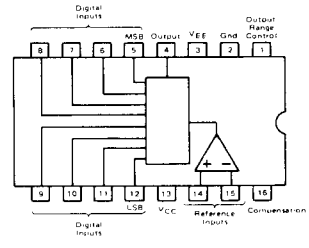


MC1507
−55 to 125°C
L Suffix — Case 620

MC1407
0 to 70°C
L Suffix — Case 620

MC1507/MC1407 — Tracking or Successive Approximation A/D Subsystem consisting of a high slew rate operational amplifier and an adjustable dual threshold comparator.

Device Number	V_{IO} Amplifier mV Max.	I_{IB} Amplifier μA Max.	Comparator V_{TH} @ V_{REF} = 40 mV mV Range	Comparator V_{IR} mV Range	Comparator I_{sink} mA Min.
MC1507	2.0	1.5	±36 to ±44	−150 to +320	3.2
MC1407	6.0	2.5	±30 to ±50	−150 to +320	3.2

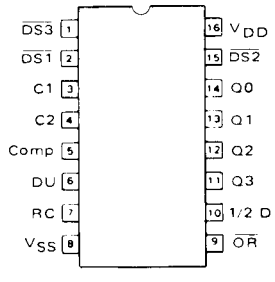


MC1508L8
−55 to 125°C
L Suffix — Case 620

MC1408L8
MC1408L7
MC1408L6
0 to 70°C
L Suffix — Case 620

8-Bit Multiplying Digital-to-Analog Converters

Device Number	Accuracy % Min.	Output Current @ V_{REF} = 2.0 V mA Range	P_D @ V_{EE} = −5.0 V mW Max.
MC1508L8	±0.19	1.9 to 2.1	170
MC1408L8	±0.19	1.9 to 2.1	170
MC1408L7	±0.39	1.9 to 2.1	170
MC1408L6	±0.78	1.9 to 2.1	170
MC3408	±0.5	1.9 to 2.1	170



MC14435
−55 to 125°C
MC14435EFL — L-Suffix Case 620
MC14435EVL — L-Suffix Case 620

−40 to 85°C
MC14435FL — L-Suffix Case 620
MC14435FP — P-Suffix Case 648
MC14435VL — L-Suffix Case 620
MC14435VP — P-Suffix Case 648

3½ Digit BCD Subsystem for Mating with the MC1505

P_C(quiescent) @ V_{DD} − 5.0 V mW Max.	I_{OL} @ V_{DD} = 5.0 V (Digit Selects) mA Min.	I_{OL} @ V_{DD} = 5.0 V (BCD Outputs) mA Min.	I_{OL} @ V_{DD} = 5.0 V (All Outputs) mA Min.
1.75	1.6	1.6	−0.2