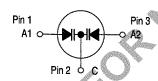
# **Silicon Tuning Diode**

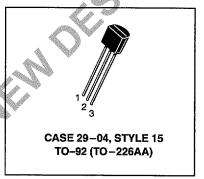
This device is designed for FM tuning, general frequency control and tuning, or any top-of-the-line application requiring back-to-back diode configurations for minimum signal distortion and detuning.

- High Figure of Merit Q = 140 (Typ) @  $V_R = 3.0$  Vdc, f = 100 MHz
- Guaranteed Capacitance Range 37–42 pF @ V<sub>R</sub> = 3.0 Vdc (MV104)
- Dual Diodes Save Space and Reduce Cost
- Monolithic Chip Provides Near Perfect Matching Guaranteed ± 1.0% (Max) Over Specified Tuning Range



# **MV104**

DUAL
VOLTAGE VARIABLE
CAPACITANCE DIODE



### **MAXIMUM RATINGS (EACH DIODE)**

		All and the second	
Rating	Symbol	Value	Unit
Reverse Voltage	VR	32	Vdc
Forward Current	JF .	200	mAdc
Total Power Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	280 2.8	mW mW/°C
Junction Temperature	TJ	+125	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C

# ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted) (EACH DIODE)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage (I <sub>R</sub> = 10 μAdc)	V <sub>(BR)R</sub>	32	_	_	Vdc
Reverse Voltage Leakage Current T <sub>A</sub> = 25°C (V <sub>R</sub> = 30 Vdc) T <sub>A</sub> = 60°C	lR	_		50 500	nAdc
Diode Capacitance Temperature Coefficient (Vp = 4,0 Vdc, f = 1.0 MHz)	TCC	_	280		ppm/°C

	C <sub>T</sub> , Diode Capacitance V <sub>R</sub> = 3.0 Vdc, f = 1.0 MHz pF		Q, Figure of Merit $V_R = 3.0 \text{ Vdc}$ f = 100  MHz		C <sub>R</sub> , Capacitance Ratio C <sub>3</sub> /C <sub>30</sub> f = 1.0 MHz	
Device	Min	Max	Min	Тур	Min	Max
MV104	37	42	100	140	2.5	2.8

#### TYPICAL CHARACTERISTICS (Each Diode)

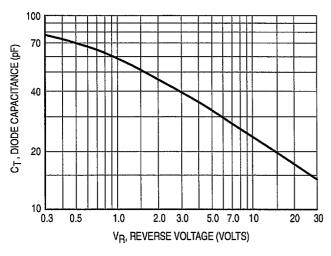


Figure 1. Diode Capacitance (Each Diode)

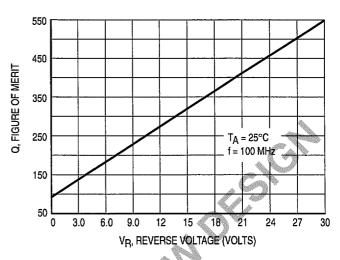


Figure 2. Figure of Merit versus Voltage

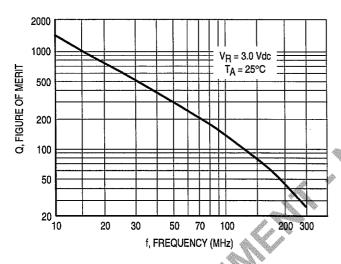


Figure 3. Figure of Merit versus Frequency

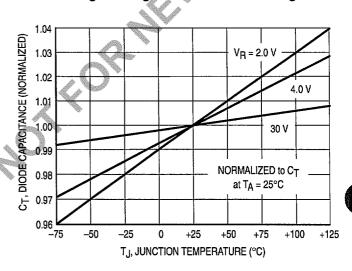


Figure 4. Diode Capacitance versus Temperature

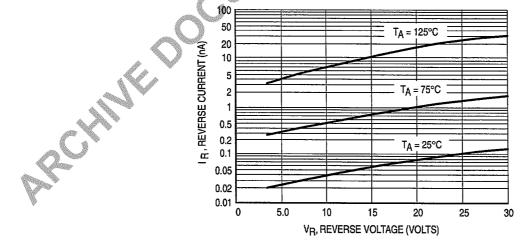
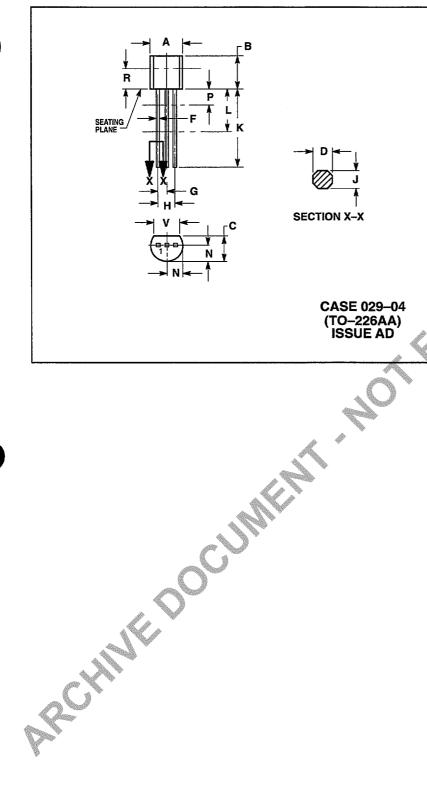


Figure 5. Reverse Current versus Reverse Voltage

#### PACKAGE DIMENSIONS



- NOTES:

  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

  2. CONTROLLING DIMENSION: INCH.

  3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.

  4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSION D AND J APPLY BETWEEN L AND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED. IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
A	0.175	0.205	4.45	5.20	
В	0.170	0.210	4,32	5.33	
C	0.125	0.165	3.18	4.19	
D	0.016	0.022	0.41	0.55	
F	0.016	0.019	0.41	0.48	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.015	0.020	0.39	0.50	
K	0.500	_	12.70	_	
L	0.250	. —	6.35		
N.	0.080	0.105	2.04	2.66	
P		0.100	-	2.54	
R	0.115	_	2.93		
V	0.135		3.43		

STYLE 15:
PIN 1. ANODE 1
2. CATHODE
3. ANODE 2

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