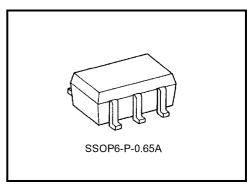
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7PA53FU

#### 2-Channel Multiplexer/Demultiplexer

#### **Features**

- Ultra-high speed operation:  $t_{pd} = 0.4 \text{ ns (max)}$  @VCC = 3.6 V,  $C_{L} = 30 \text{ pF}$
- Ultra-low on resistance:  $R_{ON} = 21 \Omega \text{ (max) @V_{CC}} = 3.6 \text{ V}$
- Operating voltage range:  $V_{CC \text{ (opr)}} = 1.8 \text{ to } 3.6 \text{ V}$
- High latch-up immunity: Higher than or equal to ±500 mA
- High ESD: Higher than or equal to  $\pm 200$  V (JEITA)
  - : Higher than or equal to  $\pm 2000$  V (MIL)
- Power-down protection provided on all input pins

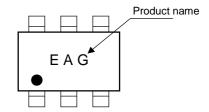


Weight: 0.0068 g (typ.)

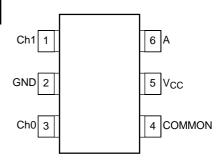
#### **Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Power supply vo	Power supply voltage		-0.5 to 4.6	V	
DC input voltage	9	V <sub>IN</sub>	-0.5 to 4.6	V	
Switch I/O voltage	Switch I/O voltage		$-0.5$ to $V_{CC} + 0.5$	V	
Clamp diode	Control input block	luz	-50	mA	
current	Switch block	IIK	±50		
Switch through of	Switch through current		100	mA	
Power dissipation		P <sub>D</sub>	200	mW	
DC V <sub>CC</sub> /ground current		Icc	±100	mA	
Storage tempera	ature	T <sub>stg</sub>	-65 to 150	°C	

#### Marking



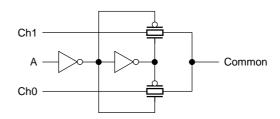
### Pin Assignment (top view)



### **Truth Table**

Input	On Channel
Α	On Channel
L	Ch0
Н	Ch1

# **System Diagram**



### **Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	1.8 to 3.6	V
Control input voltage	V <sub>IN</sub>	0 to 3.6	V
Switch I/O voltage	Vs	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
Control input rise and fall time	d <sub>t</sub> /d <sub>V</sub>	0 to 10	ns/V

# DC Electrical Characteristics ( $Ta = -40 \text{ to } 85^{\circ}\text{C}$ )

Characteristics		Symbol Test Condition		,	Min	Max	Unit
		Cymbol	rest condition	V <sub>CC</sub> (V)	IVIIII	IVIAX	Offic
	High level V <sub>IH</sub>	Vii i		1.8	0.75 × V <sub>CC</sub>		V
Input voltage		VIH		2.3 to 3.6	0.7 × V <sub>CC</sub>		
input voitage	Low level	VIL		1.8	1	0.25 × V <sub>CC</sub>	
	Low level	VIL		2.3 to 3.6		0.3 × V <sub>CC</sub>	
			$V_{IN} = 0 \text{ V}, I_O = 24 \text{ mA}$	3.6		19	Ω
			$V_{IN} = 1.9 \text{ V}, I_O = -24 \text{ mA}$	3.6		18	
		R <sub>ON</sub>	$V_{IN} = 3.6 \text{ V}, I_O = -24 \text{ mA}$	3.6	_	16	
On resistance			V <sub>IN</sub> = 0 V, I <sub>O</sub> = 24 mA	3.0	_	21	
$V_{I/O} = V_{CC}$ or GND	•		$V_{IN} = 3 \text{ V, } I_O = -24 \text{ mA}$	3.0	_	17	
VI/O = VCC OF GIAE	AI/O = ACC of GMD		V <sub>IN</sub> = 0 V, I <sub>O</sub> = 18 mA	2.3	_	25	
			$V_{IN} = 2.3 \text{ V}, I_{O} = -18 \text{ mA}$	2.3	_	20	
			V <sub>IN</sub> = 0 V, I <sub>O</sub> = 6 mA	1.8	_	32	
			V <sub>IN</sub> = 1.8 V, I <sub>O</sub> = -6 mA	1.8	_	26	
			$0 < V_{IN} < 3.6 \text{ V}, I_O = 24 \text{ mA}$	3.6	_	21	
On resistance $V_{I/O} = V_{CC}$ to GND		R <sub>ON</sub>	$0 < V_{IN} < 3 \text{ V}, I_O = 24 \text{ mA}$	3.0	_	23	Ω
			$0 < V_{IN} < 2.3 \text{ V}, I_O = 18 \text{ mA}$	2.3		42	
			$0 < V_{IN} < 1.8 \text{ V}, I_O = 6 \text{ mA}$	1.8		140	
Control input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V	3.6	_	±5.0	μΑ
Switch I/O leakage current		I <sub>SZ</sub>	V <sub>IN</sub> = 0 to 3.6 V	3.6	_	10.0	μΑ
Quiescent supply current		Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND	3.6	_	20.0	^
Increase in I <sub>CC</sub> per Input		Δl <sub>CC</sub>	V <sub>IH</sub> = 3 V	3.6		750	μА

### AC Characteristics (Ta = -40°C to 85°C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ $\Omega$ )

Characteristics	Symbol	Test Condition		Min	Max	Unit
Characteristics	Зуппоог	rest Condition	V <sub>CC</sub> (V)	IVIIII		
Propagation delay time (Note 12)	t <sub>pLH</sub>	_	1.8	_	0.7	ns
			$2.5\pm0.2$	_	0.55	
			$3.3 \pm 0.3$	_	0.4	
	<sup>t</sup> pZL <sup>t</sup> pZH	_	1.8	_	9	ns
Output enable time			$2.5\pm0.2$	_	7	
			$3.3 \pm 0.3$	_	5	
Output disable time	t <sub>pLZ</sub>	_	1.8	_	9	
			$2.5\pm0.2$	_	7	ns
			$3.3 \pm 0.3$	_	5	

When  $C_L = 50$  pF, add approximately 300 ps to the maximum values above.

Note 12: The propagation delay time is the calculated RC time constant of the typical on-state resistance of the switch and a load capacitance.

### Capacitive Characteristics (Ta = 25°C)

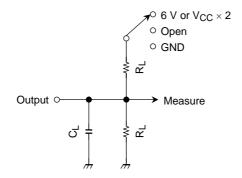
Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>	_		1.8, 2.5, 3.3	3.0	pF
Power dissipation capacitance	C <sub>PD</sub>	$f_{IN} = 10 \text{ MHz}$	(Note 13)	1.8, 2.5, 3.3	5.5	pF

Note 13: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

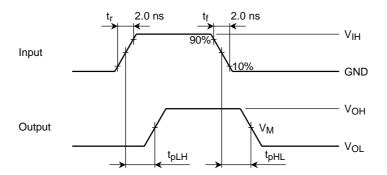
Figure 1 AC Test Circuit



Characteristics	Switch		
t <sub>pLH</sub> , t <sub>pHL</sub>	Open		
	6 V	@ $V_{CC} = 3.3 \pm 0.3 \text{ V}$	
t <sub>pLZ</sub> , t <sub>pZL</sub>	V22 × 2	@ $V_{CC} = 2.5 \pm 0.2 \text{ V}$	
	V <sub>CC</sub> × 2	$@ V_{CC} = 1.8 V$	
t <sub>pHZ</sub> , t <sub>pZH</sub>	GND		

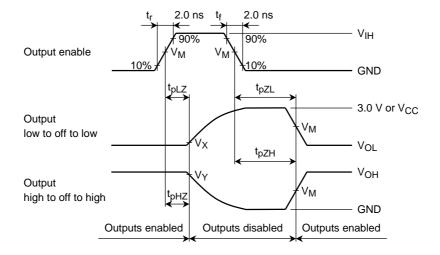
#### **AC Waveforms**

# Figure 2 $t_{pLH}$ , $t_{pHL}$



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 $\textbf{Figure 3} \quad t_{\text{pLZ}}, t_{\text{pHZ}}, t_{\text{pZL}}, t_{\text{pZH}}$ 



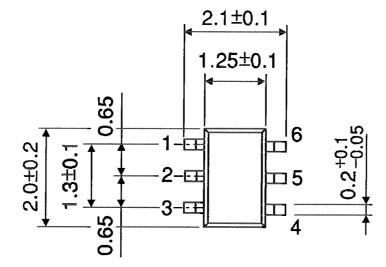
Symbol	Vcc				
Symbol	$3.3\pm0.3~\textrm{V}$	$2.5\pm0.2\textrm{V}$	1.8 V		
V <sub>IH</sub>	2.7 V	V <sub>CC</sub>	V <sub>CC</sub>		
V <sub>M</sub>	1.5 V	V <sub>CC/2</sub>	V <sub>CC/2</sub>		
VX	V <sub>OL</sub> + 0.3 V	V <sub>OL</sub> + 0.15 V	V <sub>OL</sub> + 0.15 V		
VY	V <sub>OH</sub> – 0.3 V	V <sub>OH</sub> – 0.15 V	V <sub>OH</sub> – 0.15 V		

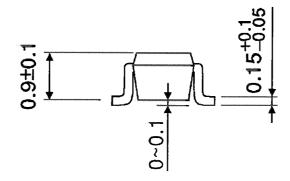
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### **Package Dimensions**

SSOP6-P-0.65A

Unit: mm





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Weight: 0.0068 g (typ.)

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